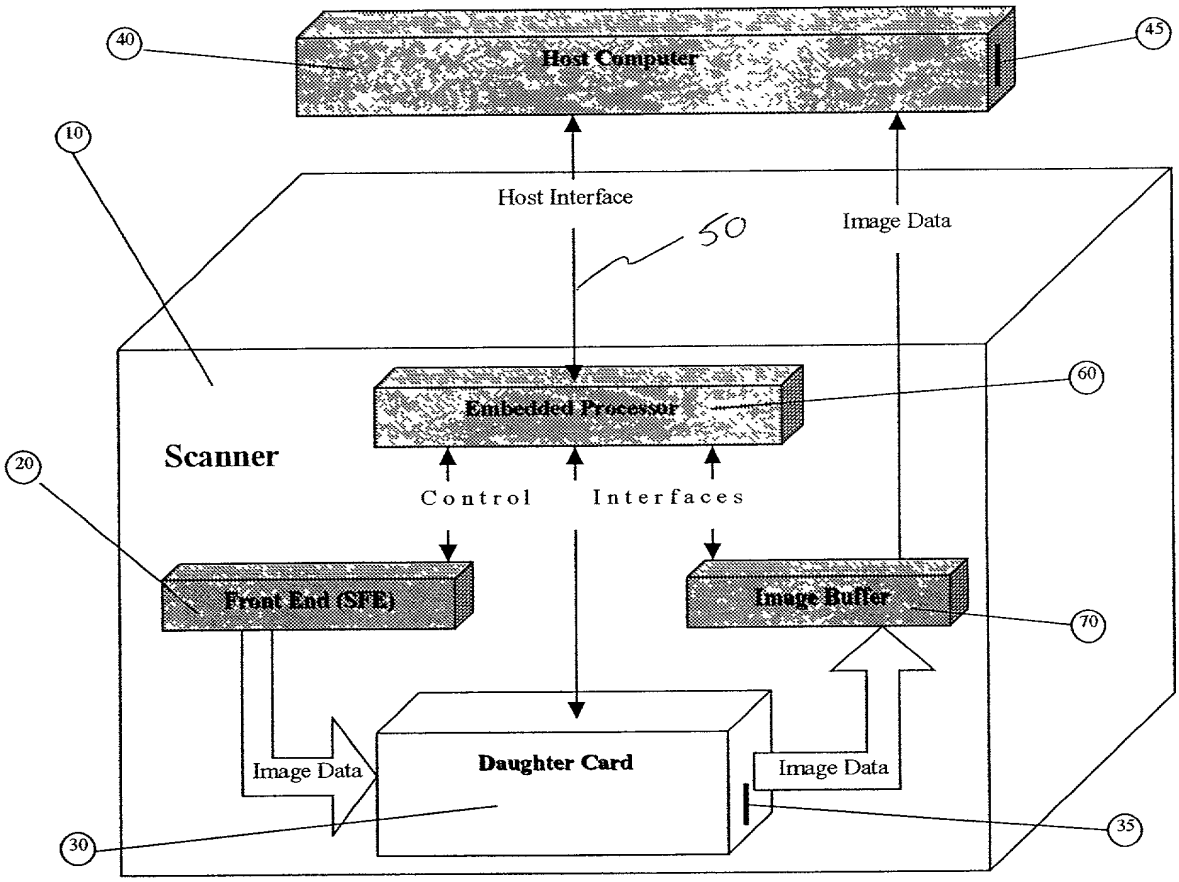
Figure 1


40
Host Computer
45
10
Host Interface
50
Image Data
Embedded Processor
60
Scanner
20
Control
Interfaces
Front End (SFE)
Image Buffer
70
Image Data
Daughter Card
Image Data
30
35